



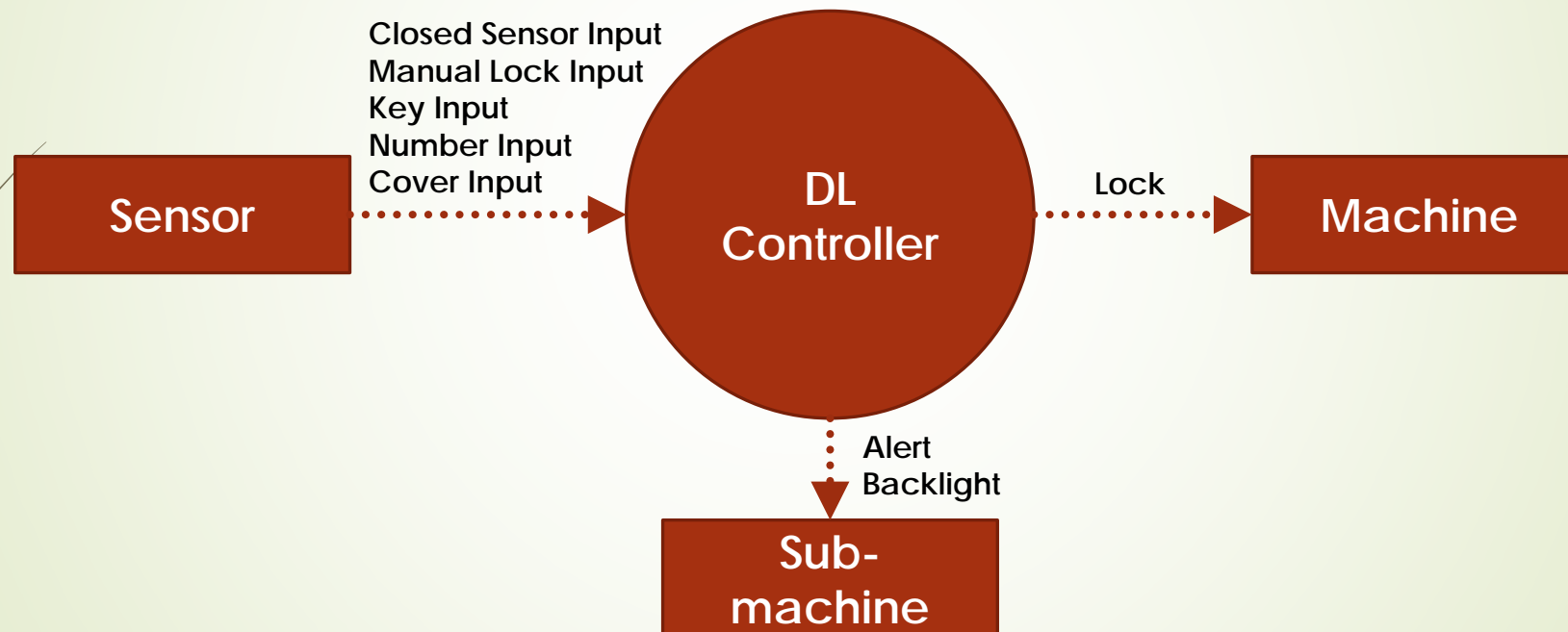
Software Requirement Analysis

Electronic Door Lock System

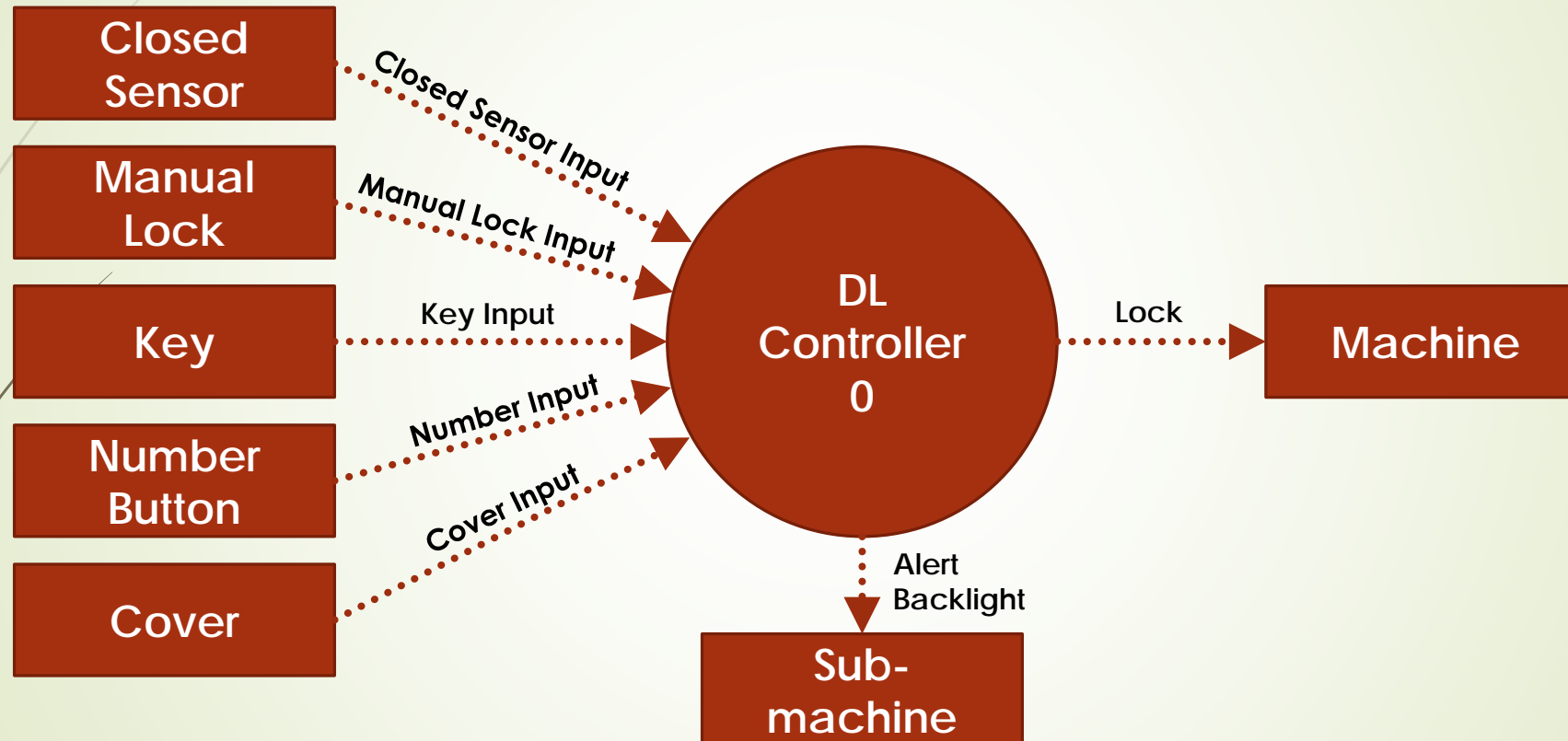
7 Team

200810773 차소익, 201011364 정광용

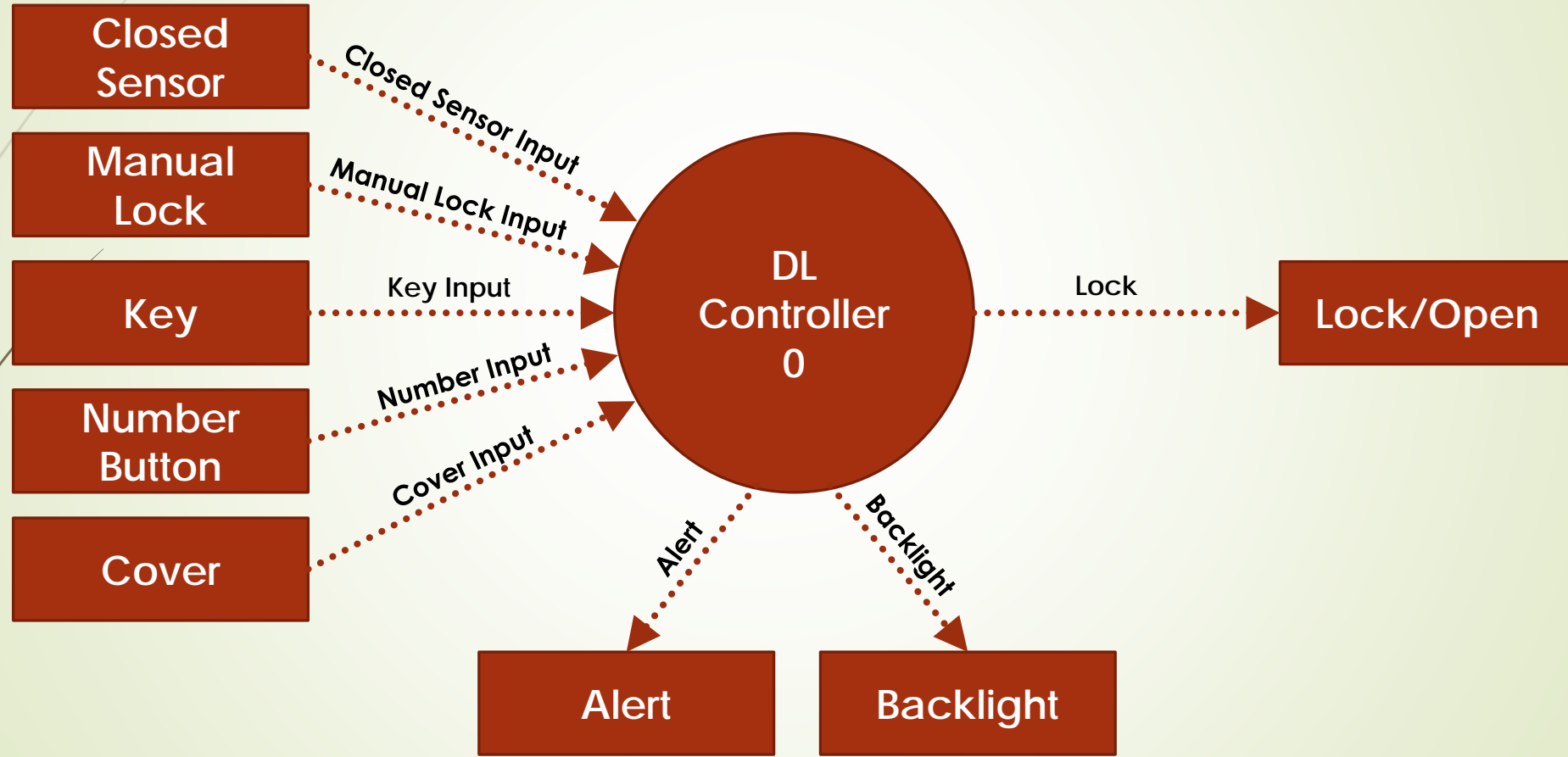
System Context Diagram



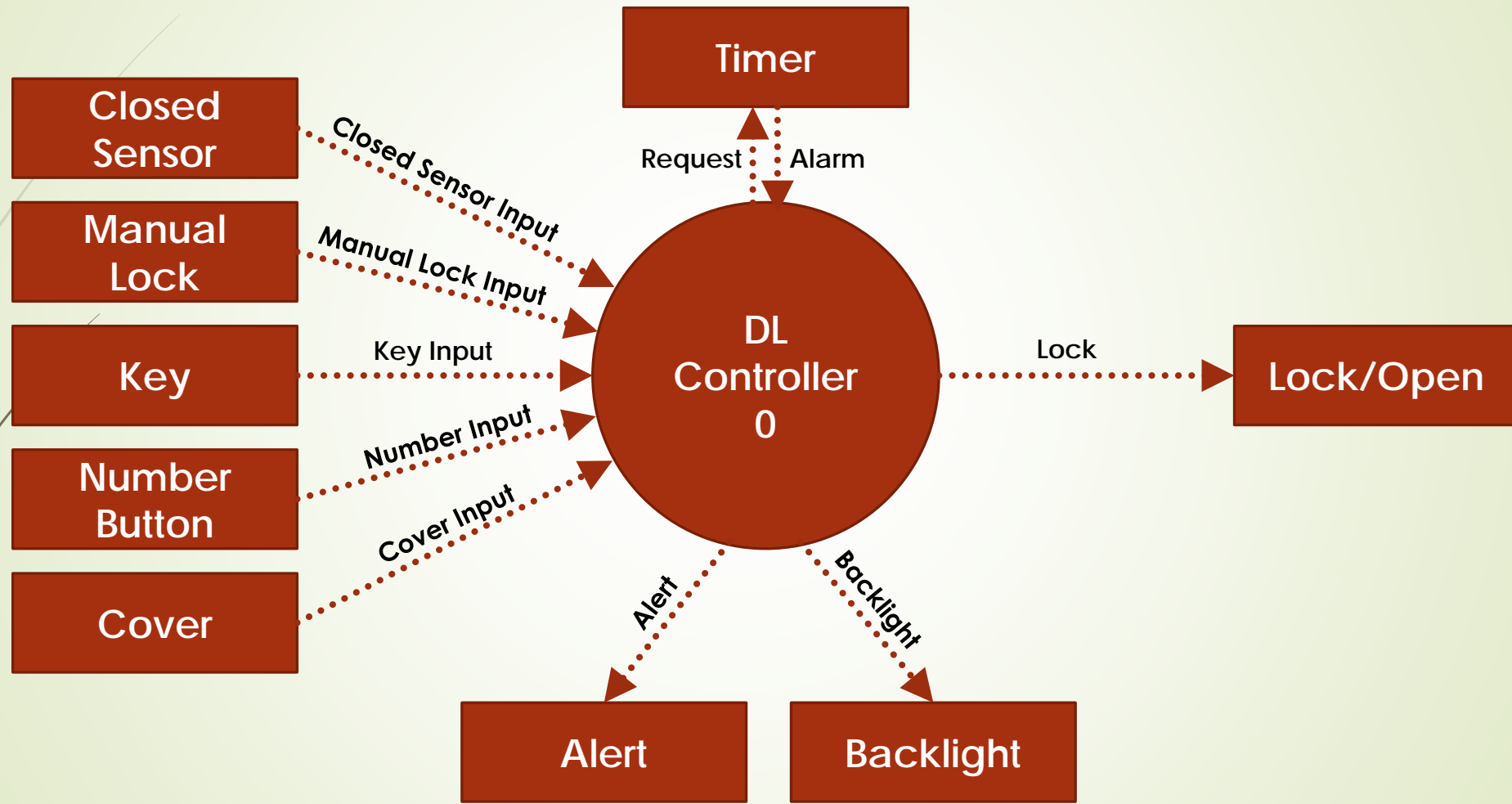
DFD Level 0



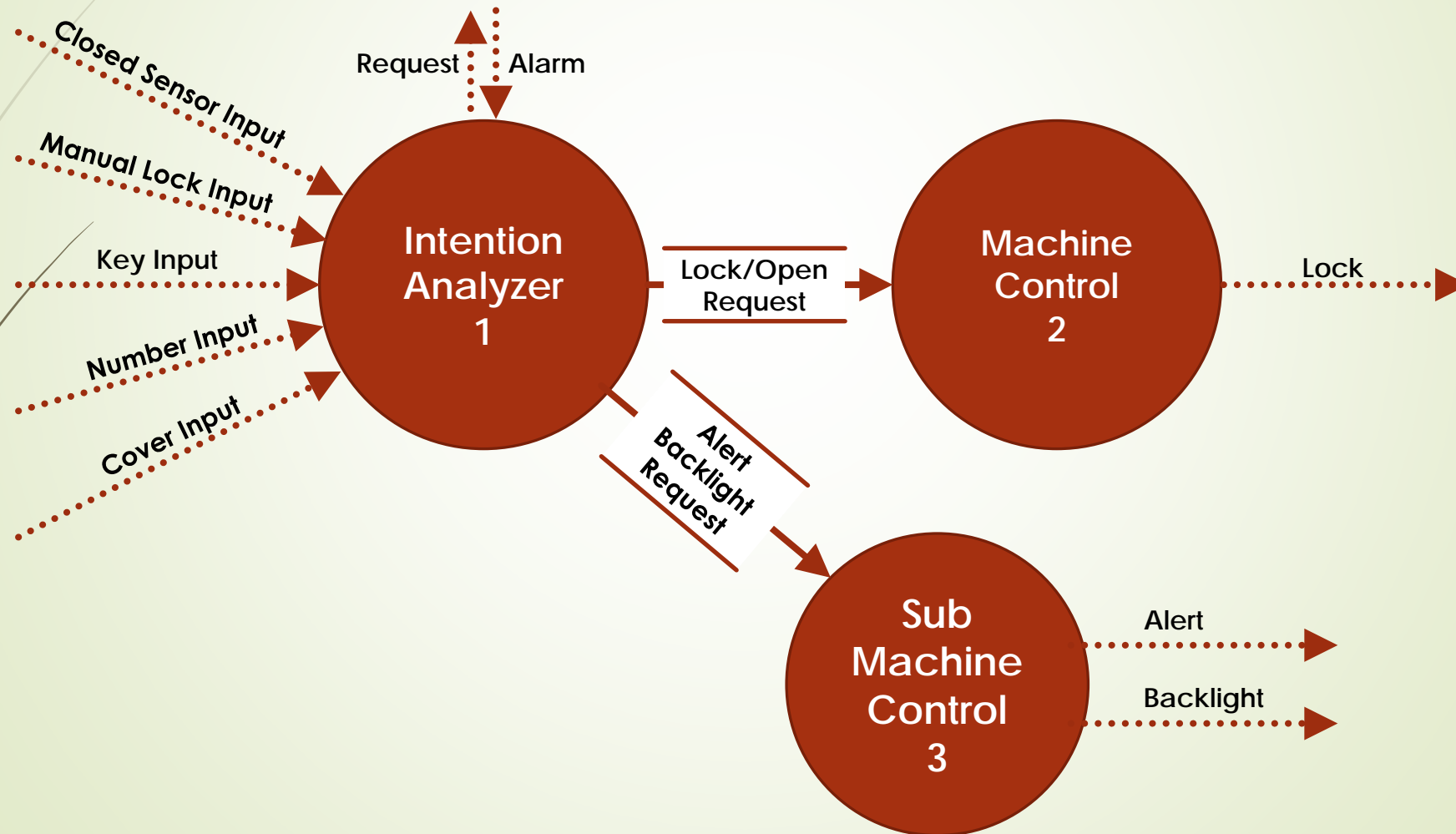
DFD Level 0



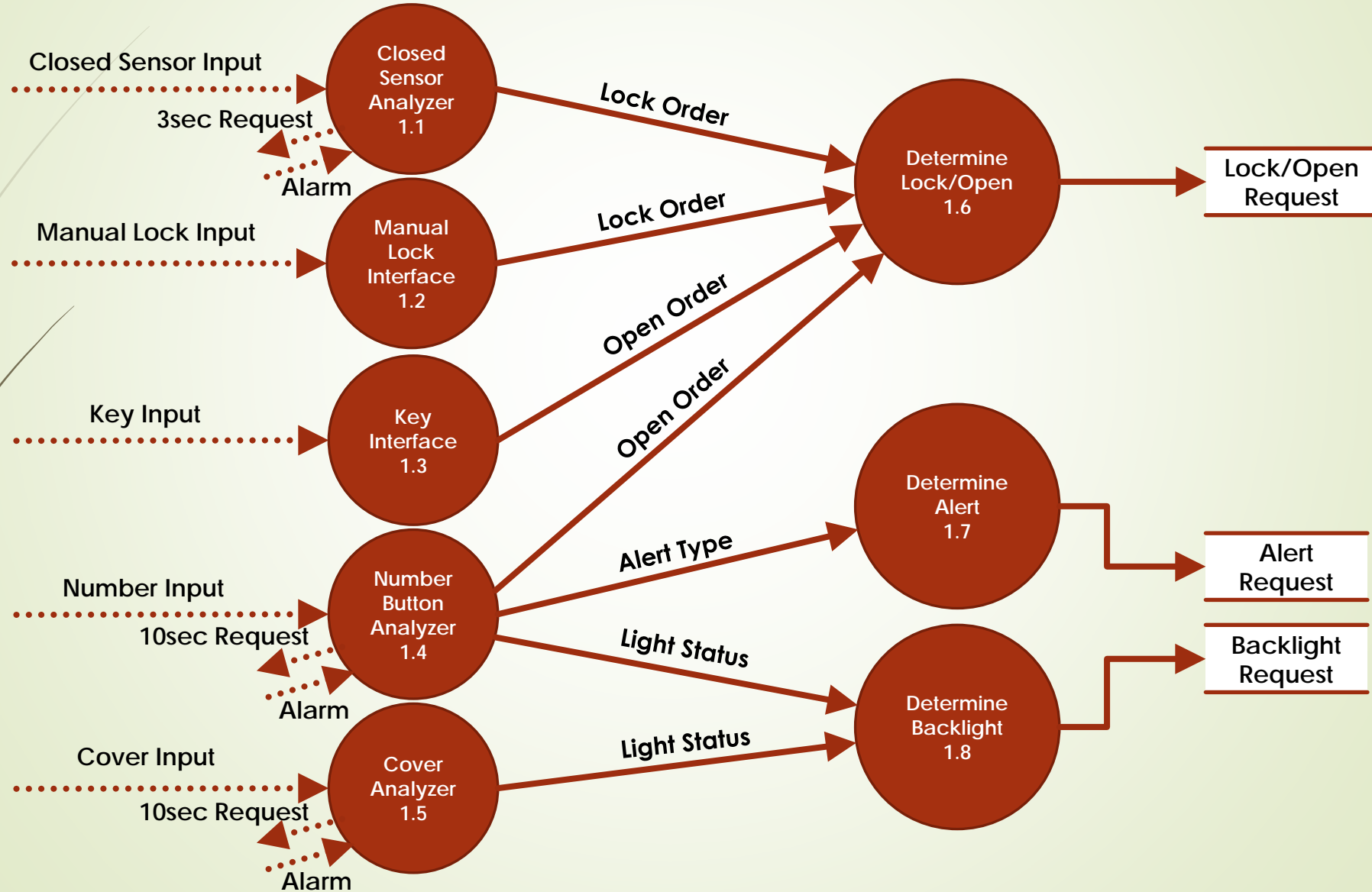
DFD Level 0



DFD Level 1

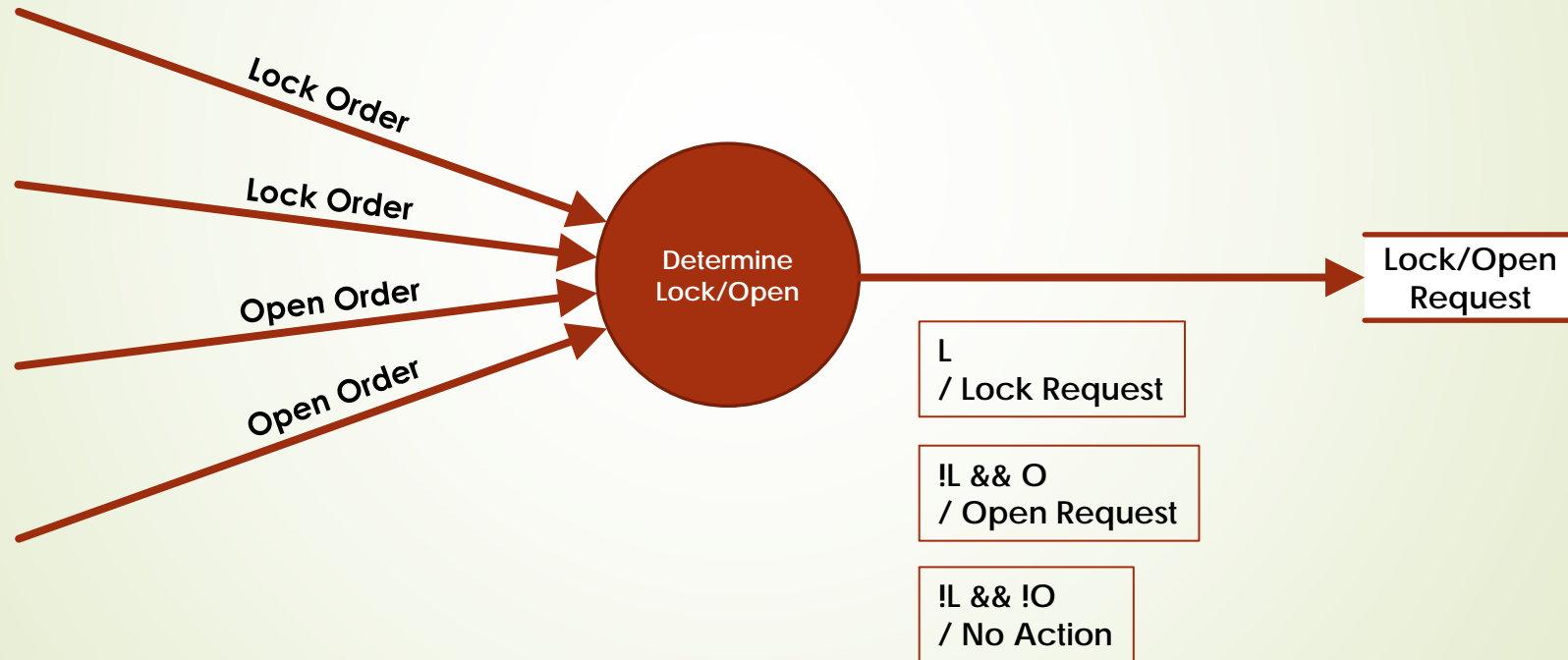


DFD Level 2



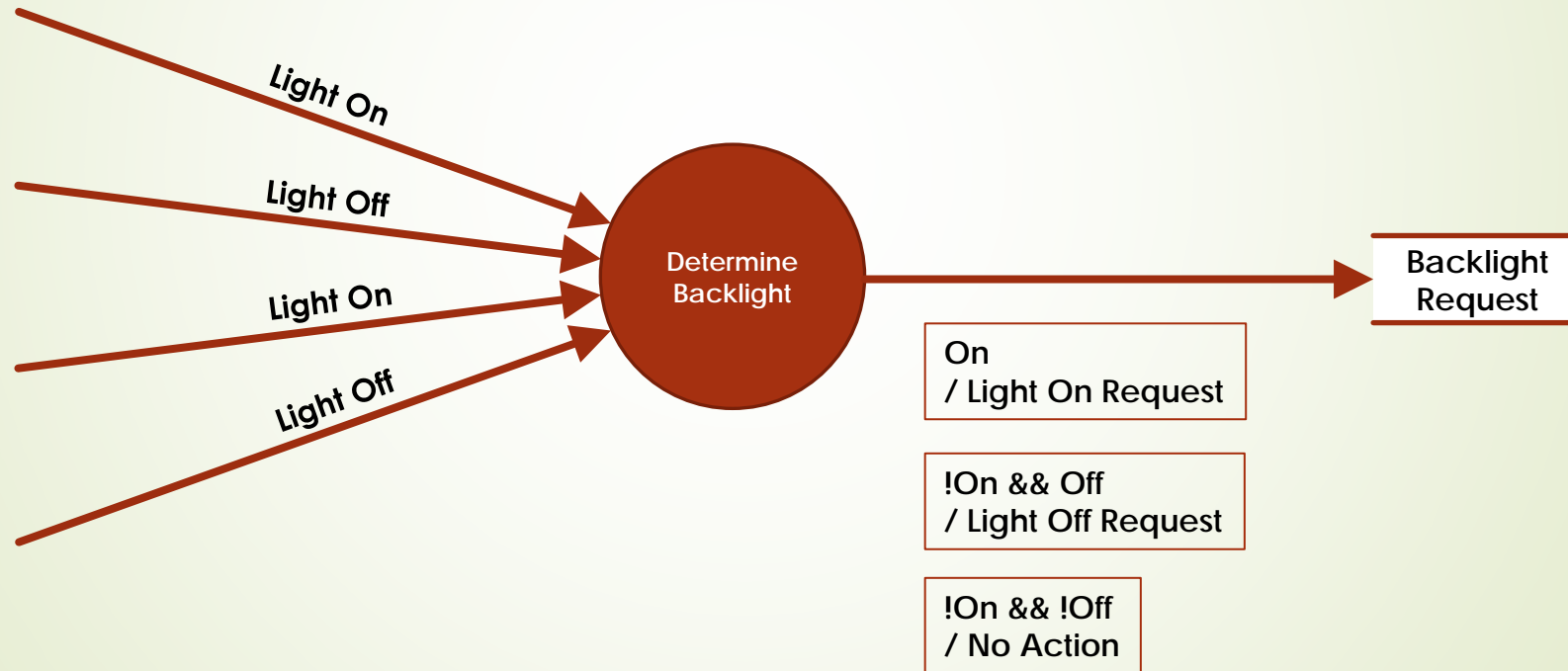
DFD Level 2

State Transition Diagram for Determine Lock/Open 1.6

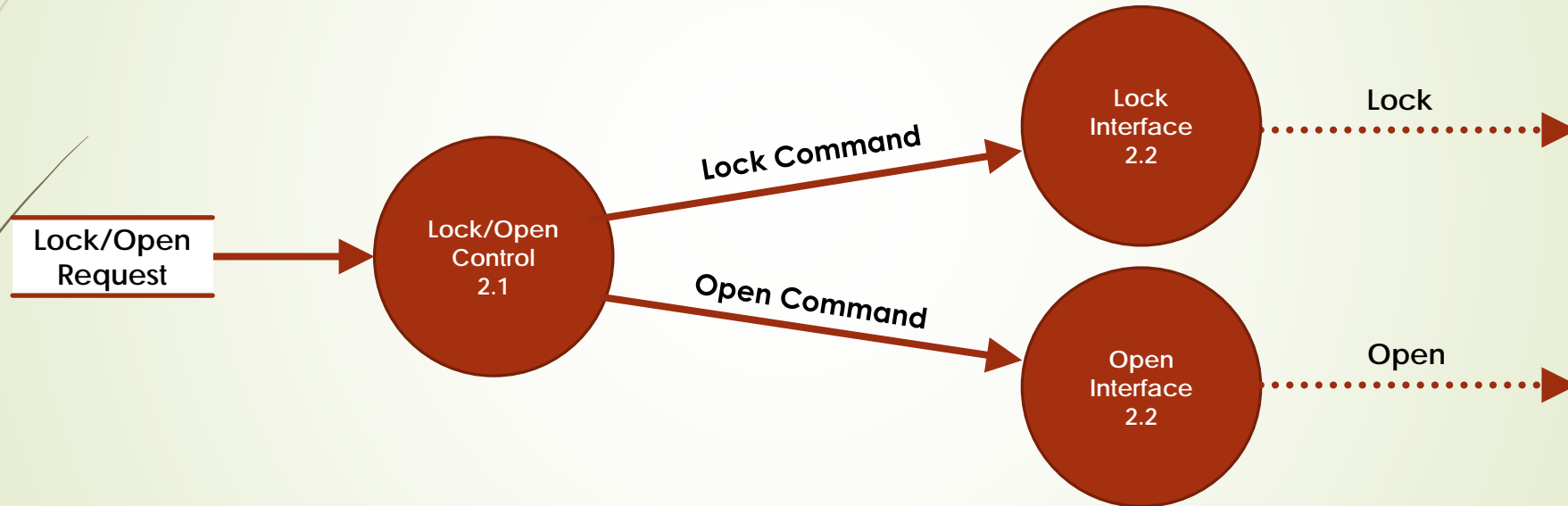


DFD Level 2

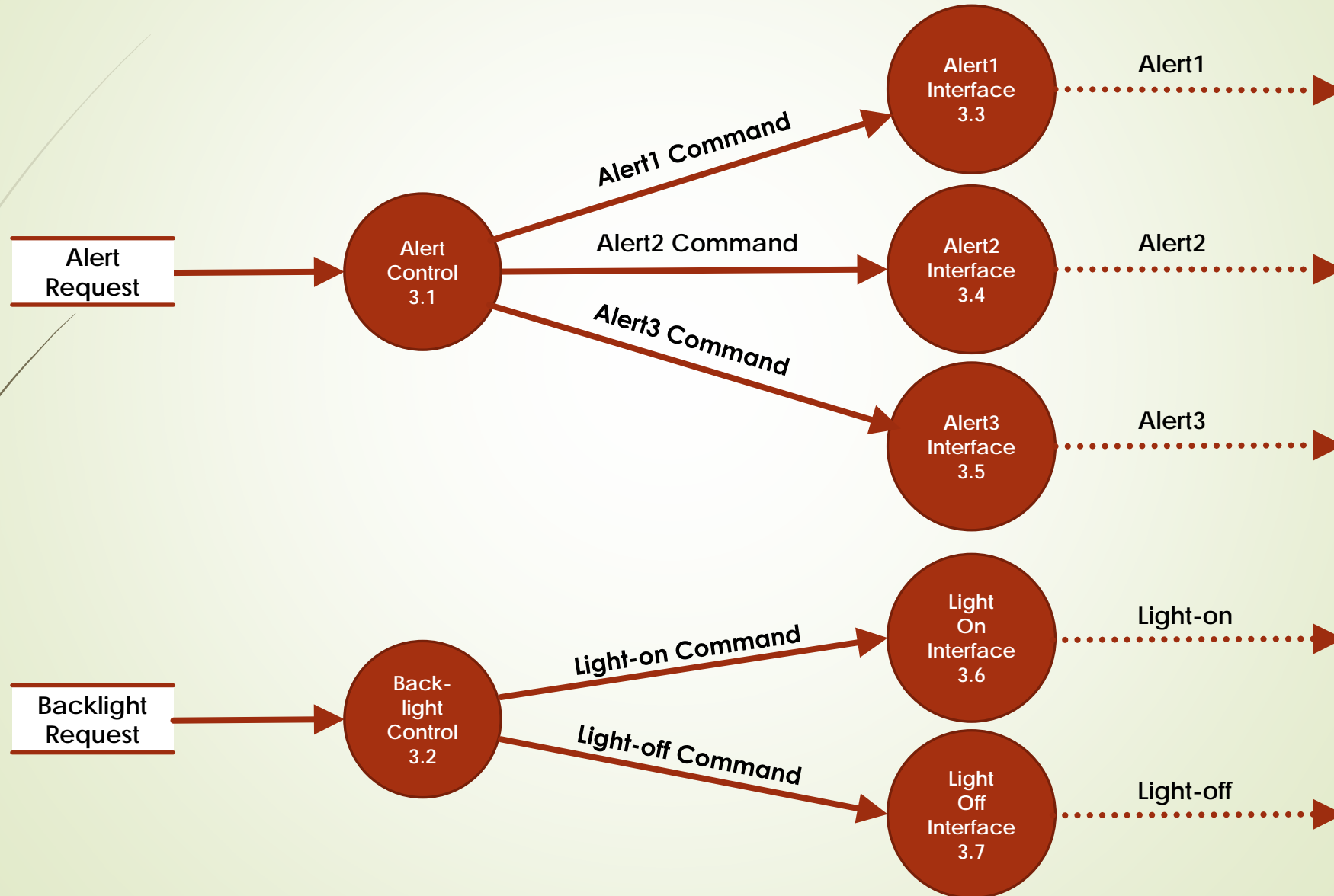
State Transition Diagram for Determine Backlight 1.8



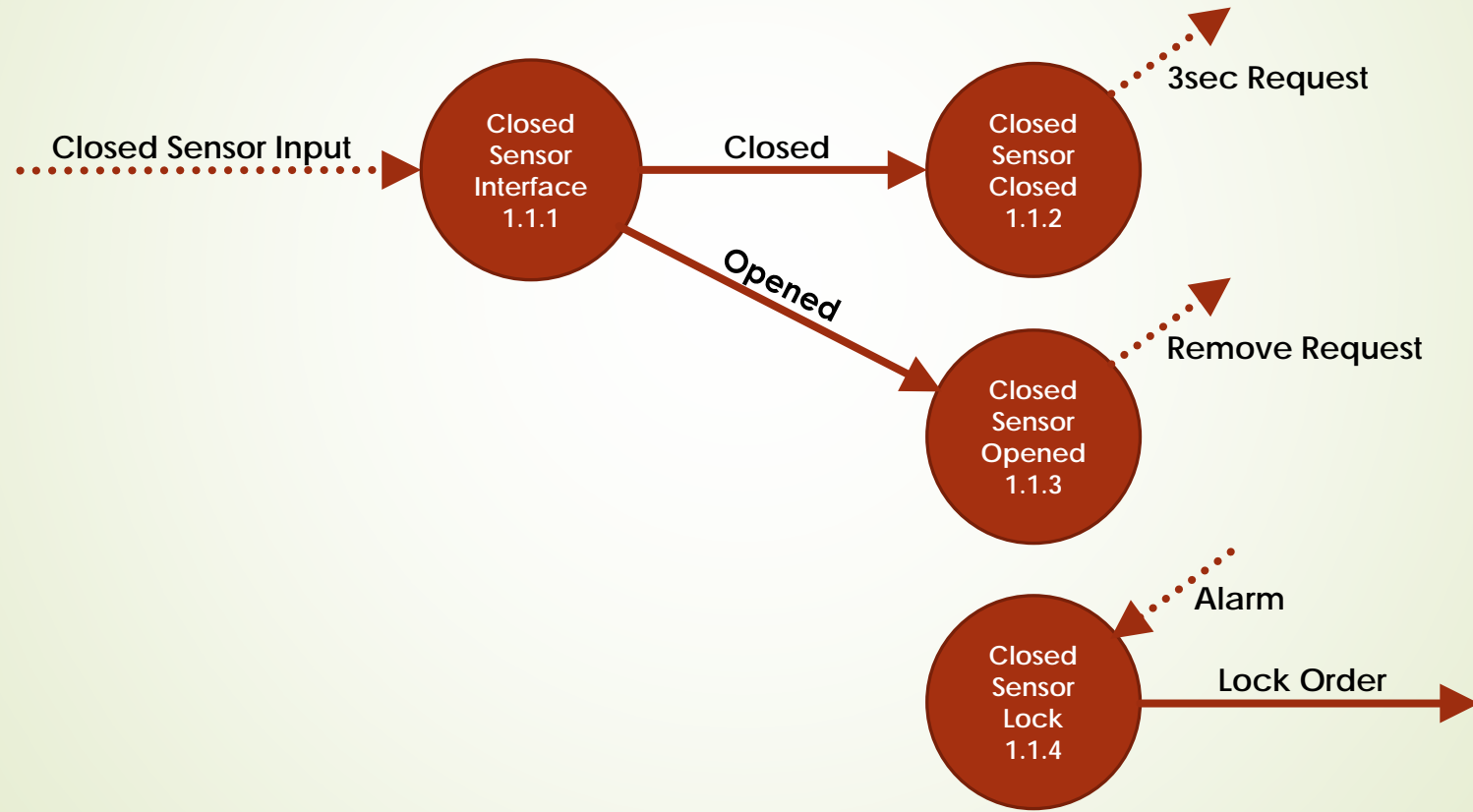
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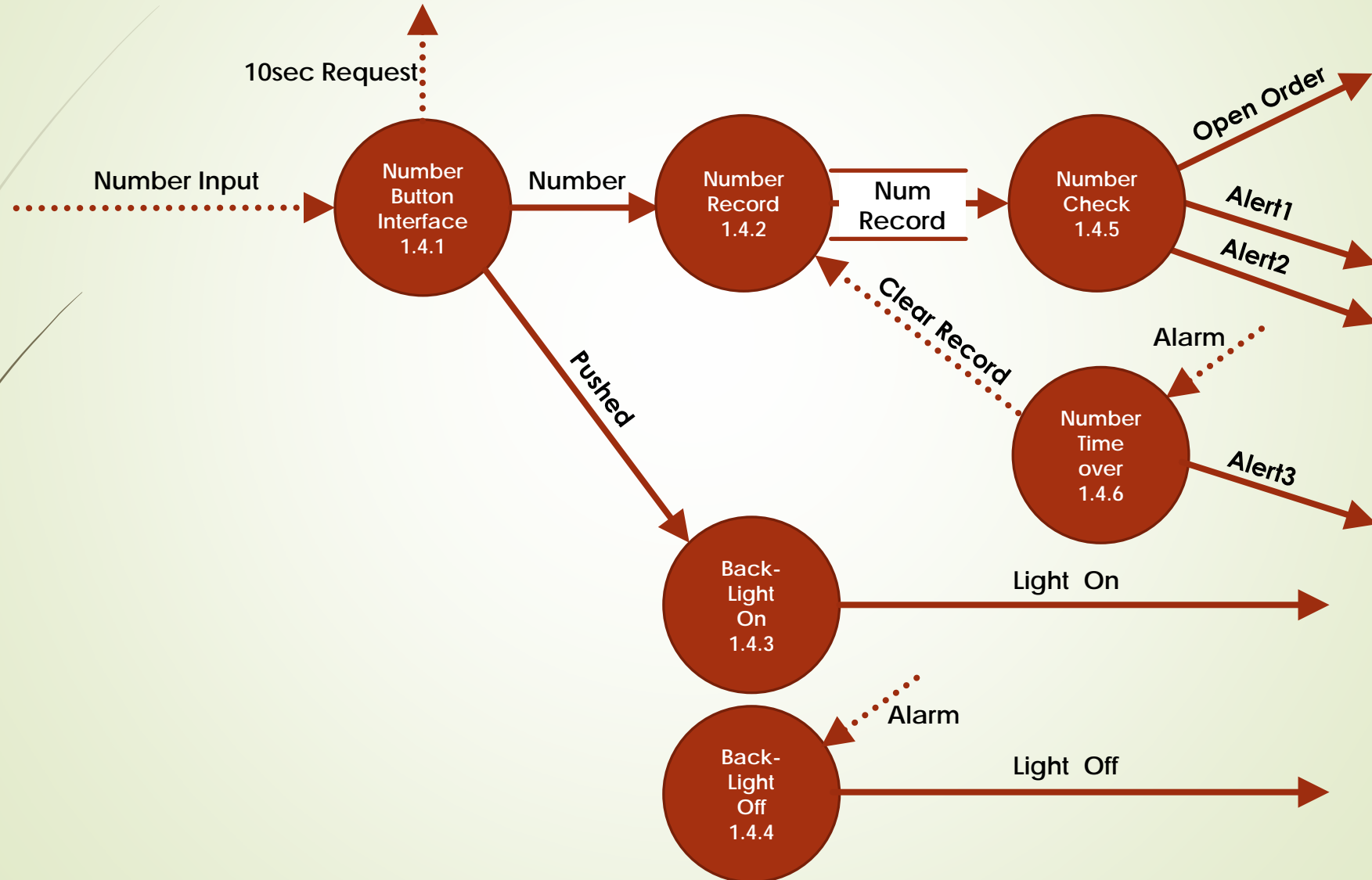
DFD Level 2



DFD Level 3

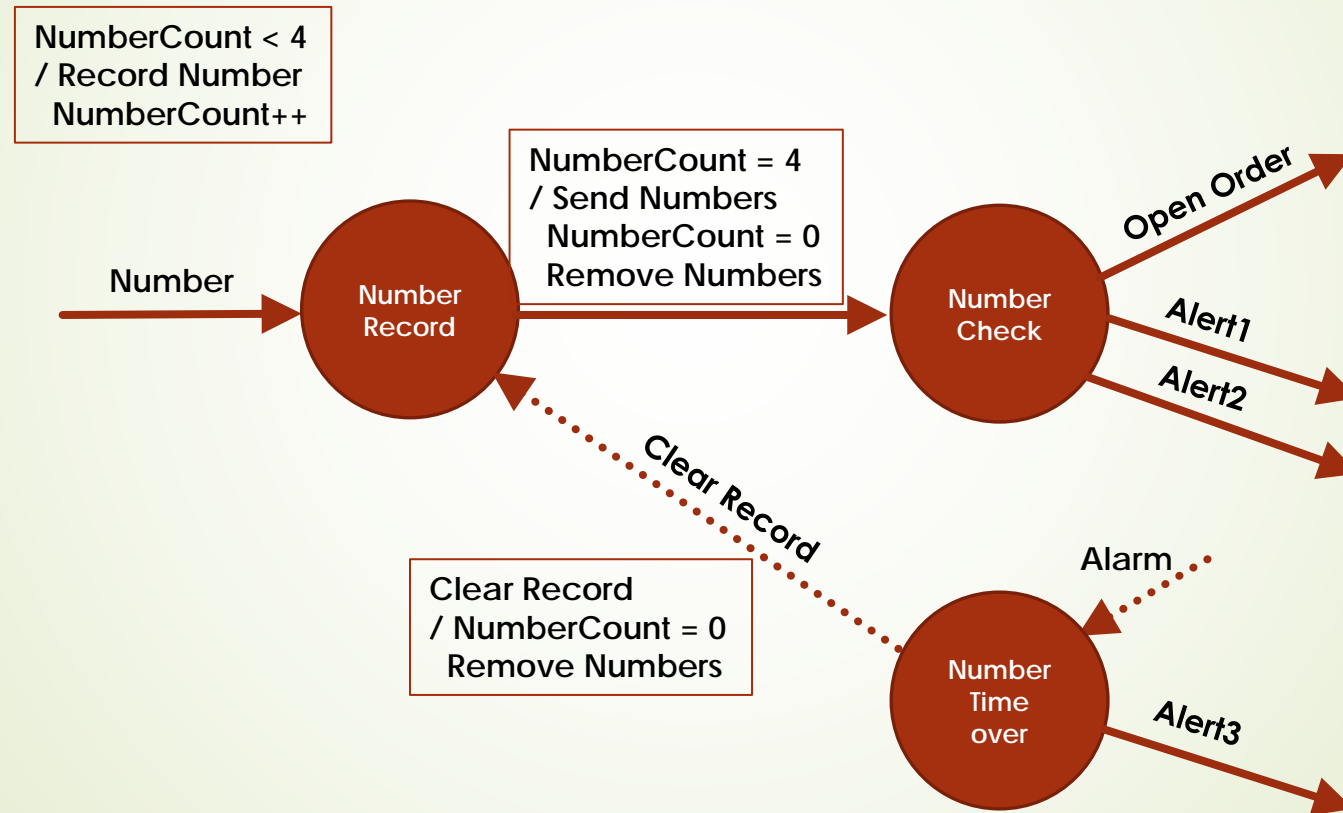


DFD Level 3

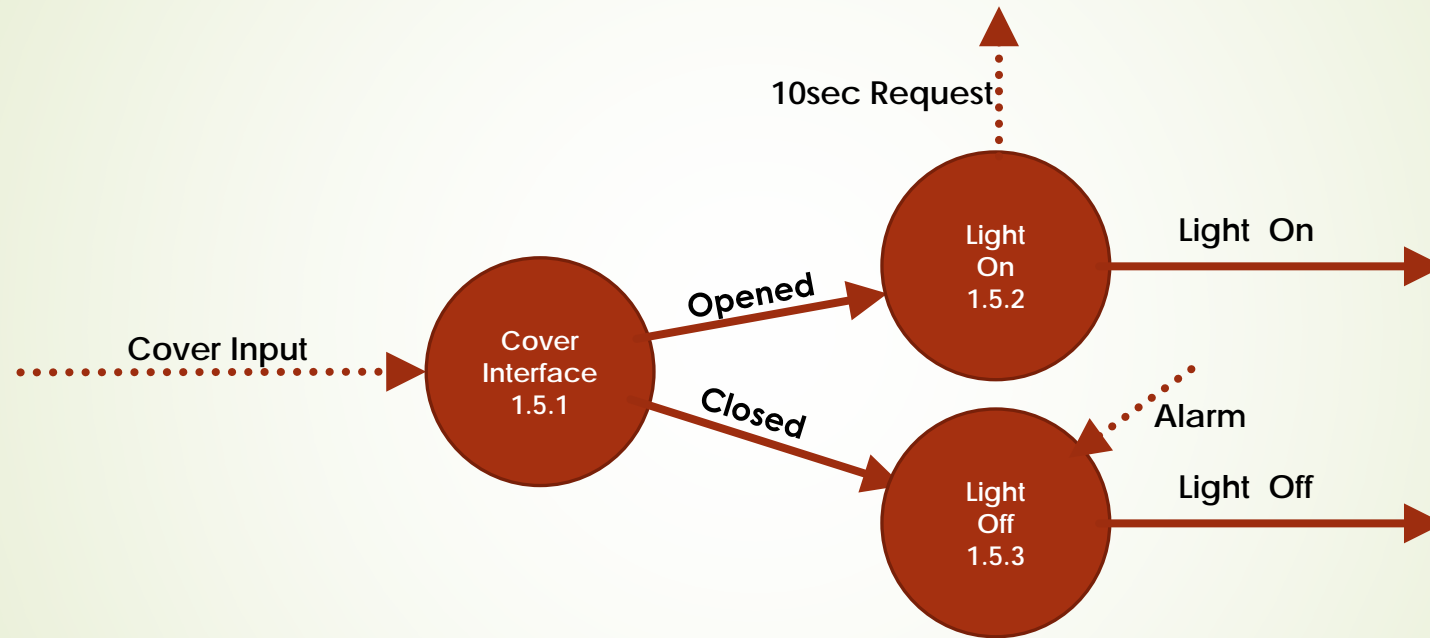


DFD Level 3

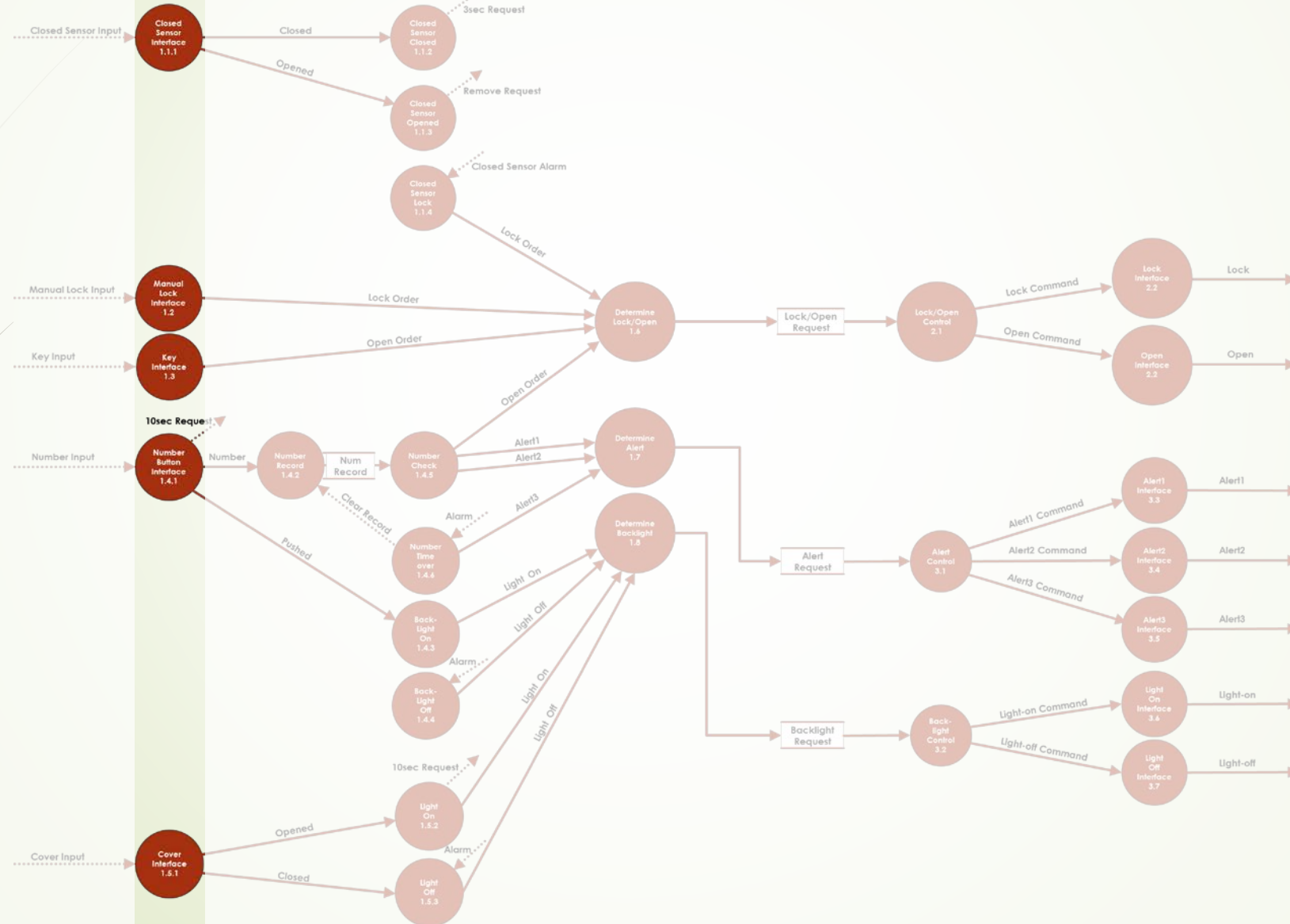
State Transition Diagram for Number Record 1.4.2



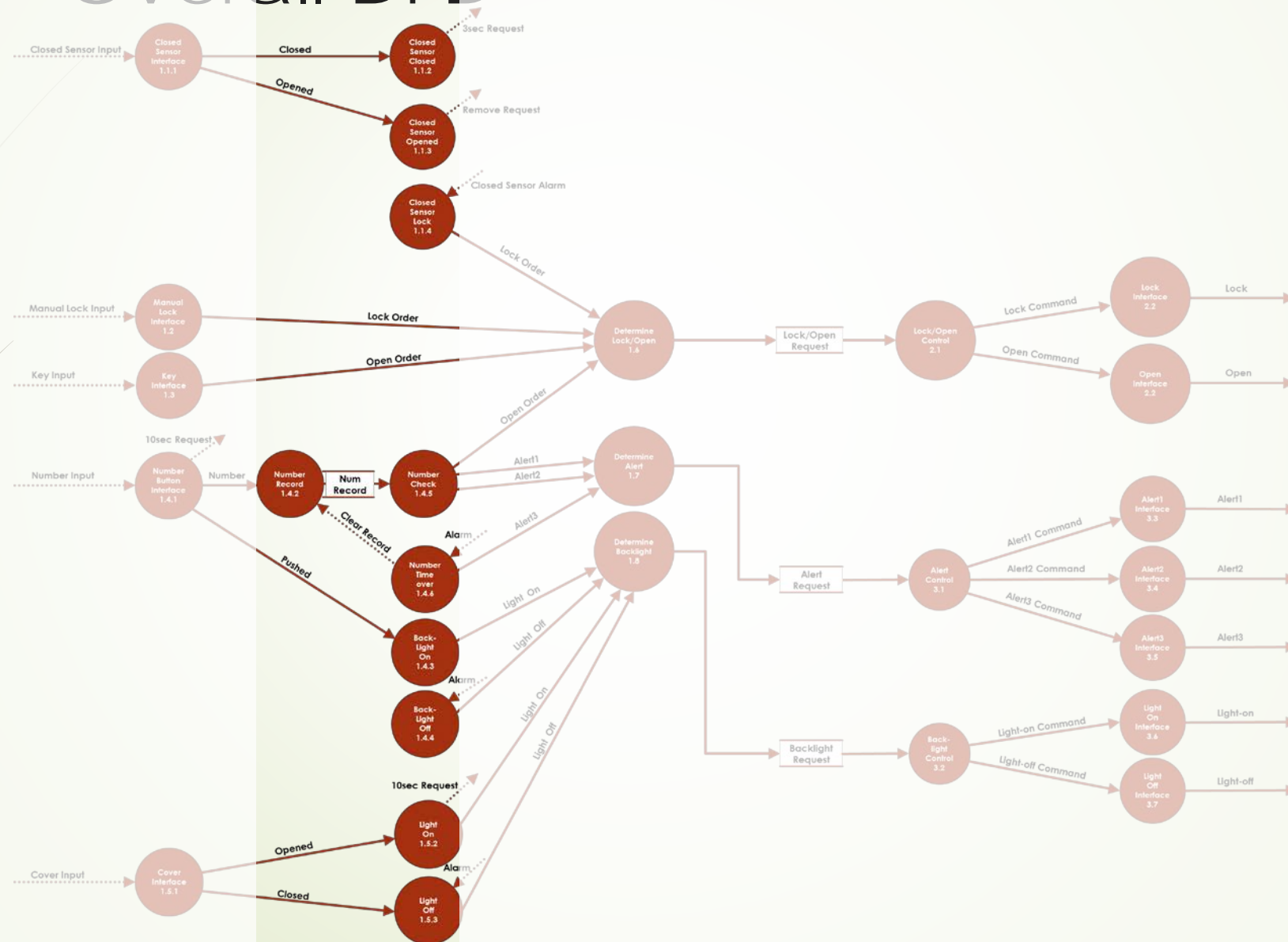
DFD Level 3



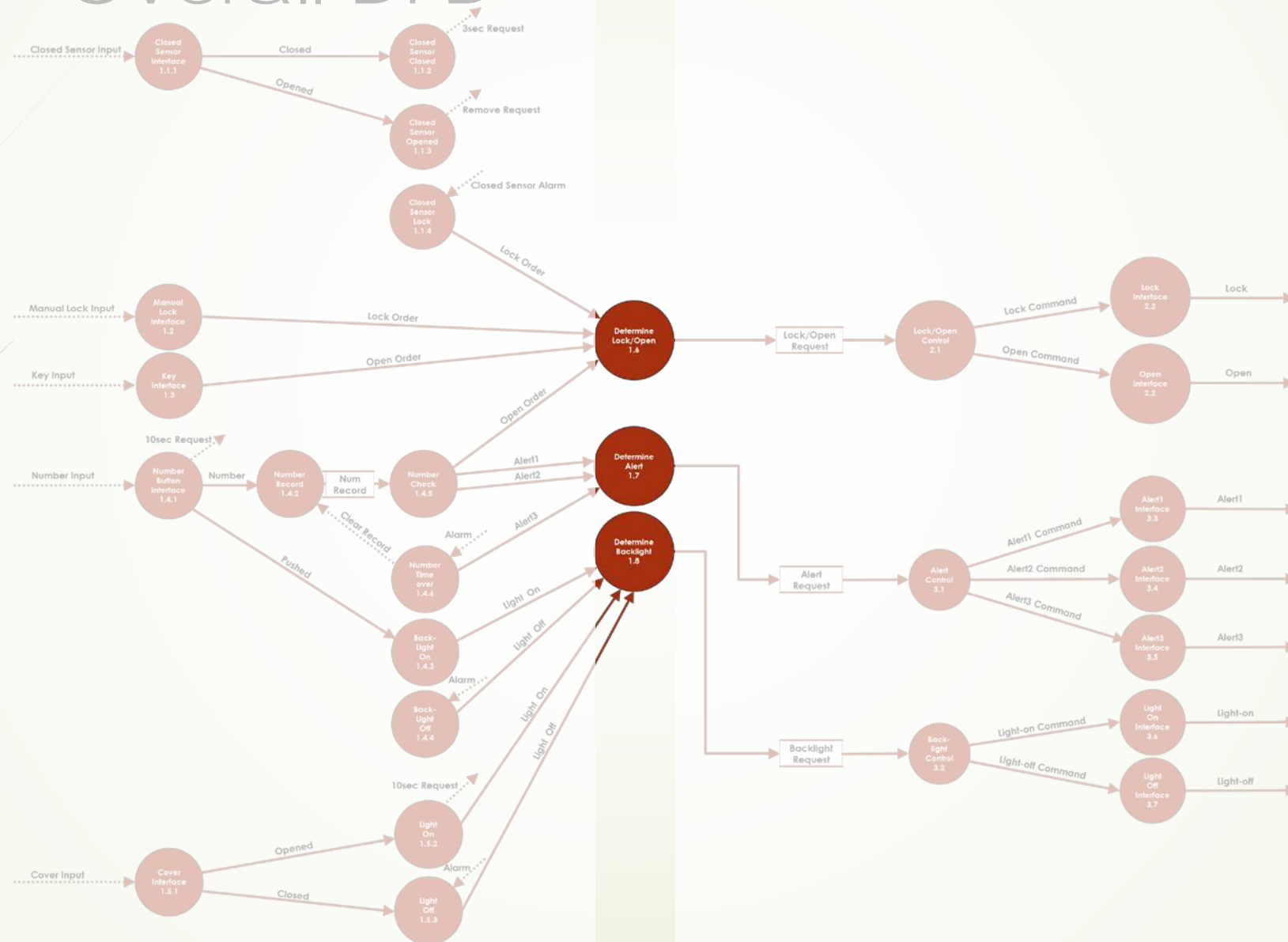
Overall DFD



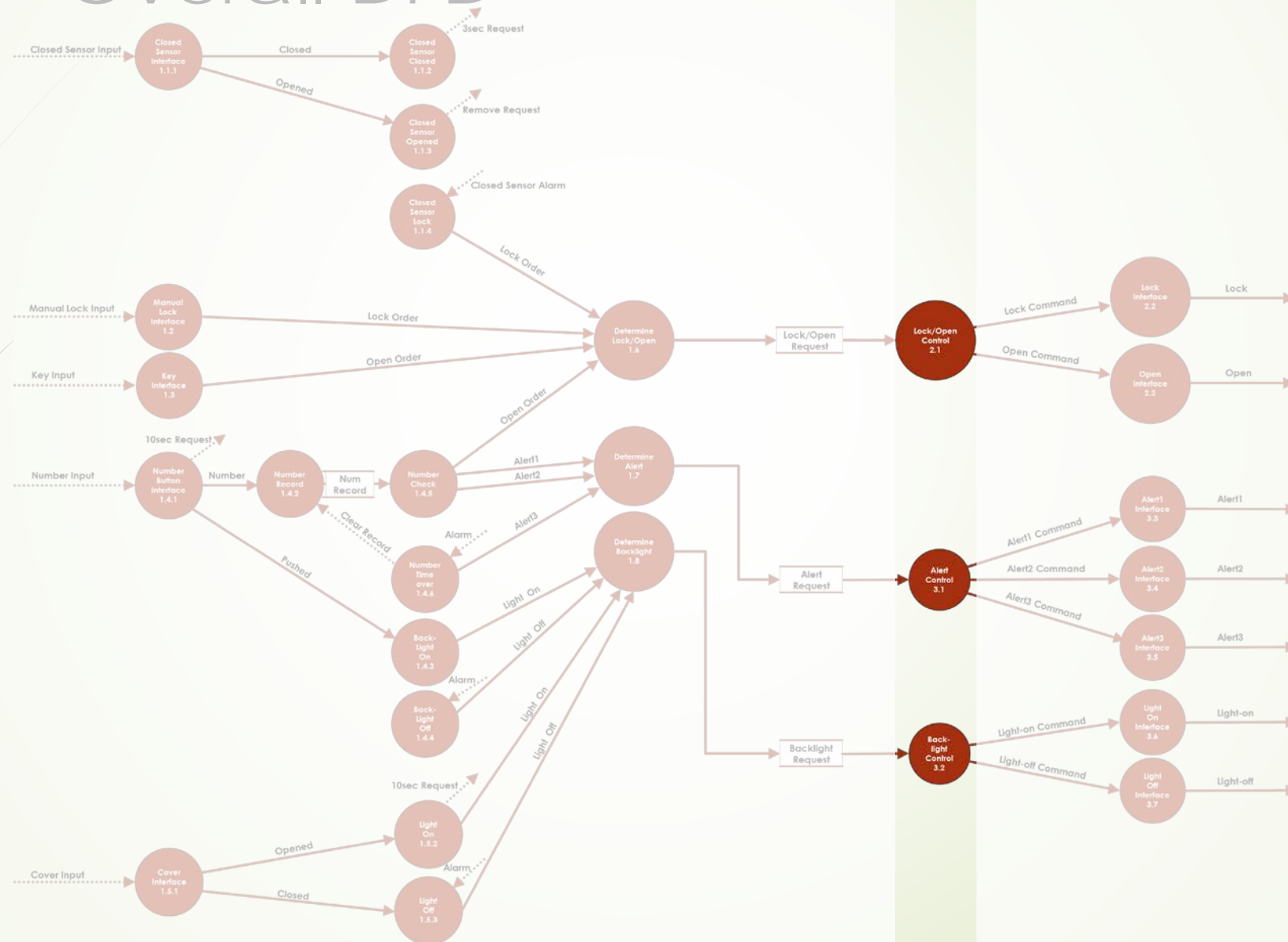
Overall DFD



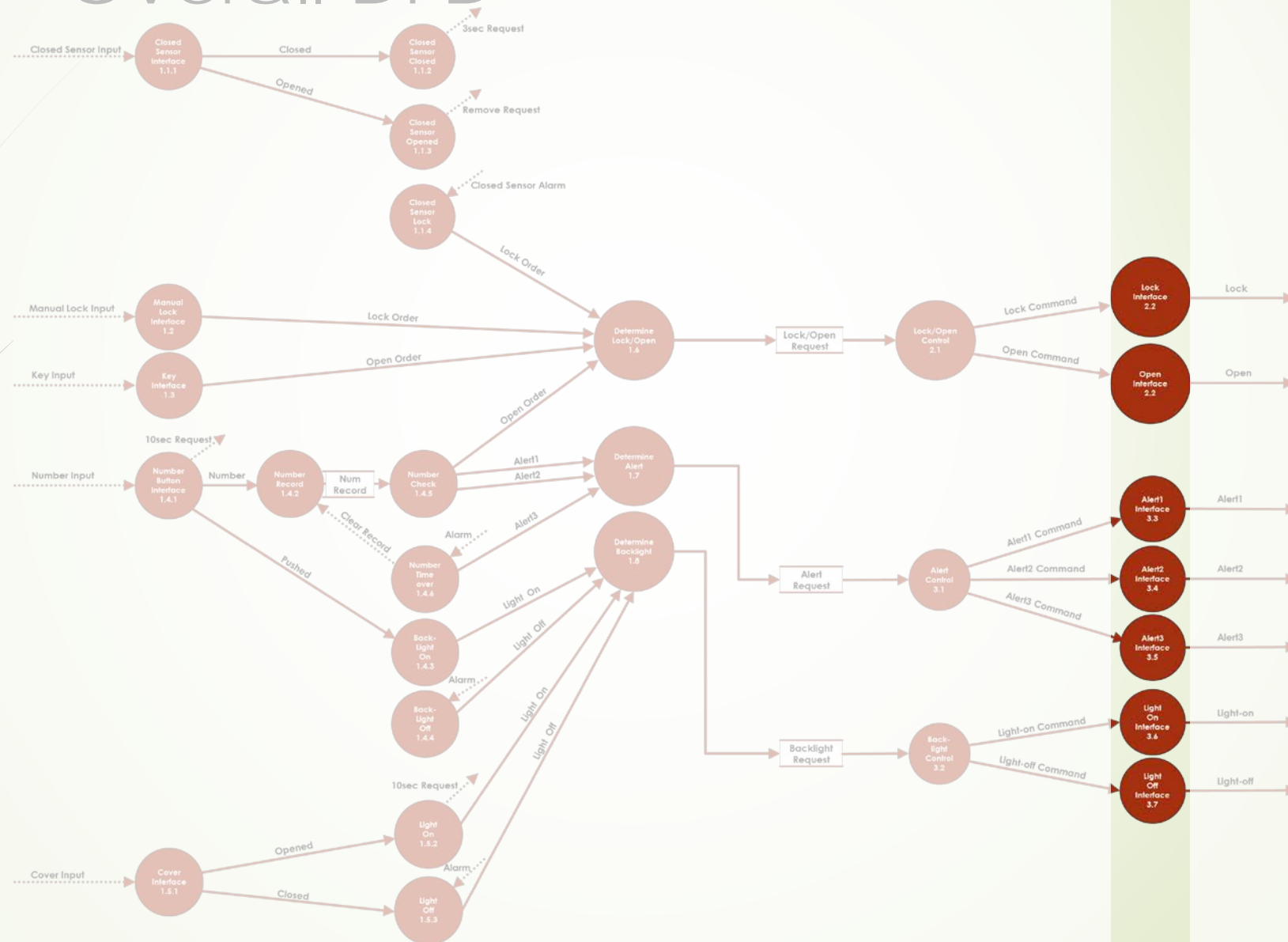
Overall DFD



Overall DFD



Overall DFD



Overall DFD

