

**Business from technology** 

# Ontology-Driven Natural Language Requirement Templates for Model Checking I&C Functions

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# **Controlled Natural Language (CNL) in requirements definition**

#### Motivation

- Poor quality of textual requirements (vagueness, complexity of sentences, inexact terminology)
- Need for formalised requirements in advanced tools, e.g. model checking
- Existing research and technologies in the fields of CNL and semantic web

#### The approach

- Develop recommended natural language templates together with corresponding formal representations
- Define a vocabulary (ontology) for the domain
- Develop a concept of a "syntax and ontology controlled requirements editor"



#### **Model checking**

- Model checking is an efficient formal method for the verification of critical systems.
- Model checker is a software tool that exhaustively checks that a model of a system satisfies given requirements.
  - The answer is either **YES**...
  - ...or NO, in which case the model checker will output a *counterexample*, demonstrating an execution path that does not meet the requirement.





## **2D modelling with Simantics**

- Simantics open-source platform for modelling and simulation
- <u>https://www.simantics.org/</u>
- Specify the function block diagram in a 2D view.







#### **Browsing counter-examples with Simantics**

- Review of counter-examples with "living" function block diagrams
- In the future: automatic transformation of SMV model from design data







### **Requirement templates**

- Mapping of natural language description with the associated temporal logic
- "Fill in the blanks" refer to domain concepts (i.e. devices, functions, I&C system inputs and outputs)

Natural language template					Temporal logic representation		
	P between	occurs Q	at most and	2 times R	G((Q & F(R)) -> ((!P & !R) U (R   ((P & !R) U (R   ((!P & !R) U (R   ((P & !R) U (R   (!P U R)))))))))		



#### **Requirement templates – an example**

#### Requirement:

After the shutdown signal, valve V15 must be opened and remain open until the level in tank T4 is below 230 cm.

Na	itural langua	ge template	Temporal logic representation		
l i	V15_OPEN after until	must hold SHUTDOWN_SIGNAL (T4_LEVEL_M < 230)	G((SHUTDOWN_SIGNAL & !((T4_LEVEL_M < 230) )) -> (G(V15_OPEN)   ! V15_OPEN U (T4_LEVEL_M < 230) )))		



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#### **CNL** editor, the overall concept





### **User interface option 1 – Fill in the blanks**

ReUse - Requirement template	tool						
File Options							
Patterns	Pattern description		Select model variable				
Patterns         Dwyer et al. patterns         Occurrence         Occurrence         Occurrence         Occurrence         Order         Order         Order         Order         Order         Order         Order         Order         Order         Occurrence         Occurrence         Occurrence         Occurrence         Occurrence/Exclusion         Occurrence/Exclusion         Occurrence/Exclusion         Occurrence         Occurrence/Exclusion         Occurrence/Exclusion         Occurrence/Exclusion         Occurrence/Exclusion         Occurrence         Occurrence <thore< th=""> <thord< th=""></thord<></thore<>	Pattern description         Universality Property Pattern         Used to describe a portion of a system's execution which states that have a desired property. Also known as Hend Always.         Scope         After Q until R         Q       R       Q       Q         Template         (LINE_3.V15_OPEN) is true after Q until R         LTLSPEC G(Q & !R -> (G(LINE_3.V15_OPEN)   ((LINE_3.VIR)))	Proposition editor Q = Syntax check: OK	Select model variable				
			Close				



#### **Option 2 - Editing guided by templates with autocomplete**

SCNL editor								
File Edit Help								
Model elemen	Model elements Statements							
Project: ReUse-R S-001: Condition START ACT holds after condition SWS010001 MANUAL COMMAND until cond					Universality after			
Generative S-002: Condition ( OR2.BO implies ( DELAY3s 2.BO OR DELAY15s.BO ) ) holds globally.					Universality after until			
– 🗋 cls: Device								
← I cls: Function								
- 🗋 cls: Space	Cls: Space							
AND1.BO								
DELAY15s.BO					<b>T</b>			
- 🗋 DELAY3s.BO	New C	NL statement			<u>×</u>			
DELAY3s_2.BO	Option	s: <boolean></boolean>						
OR1.BO	Condi	tion (START_ACT implies DELAY3s.BO) holds after condition NOT de						
OR2.BO								
START_ACT								
					Next terms			
SWSPI0005 RATE C					ID1.BO			
U DELAY1.BO					LAY15s.BO			
∽ 🚍 cls: Numeric 1	Templa	tes: Universality_after, Universality_after_until			LAY3s.BO			
⊶ 🗂 cls: Template_term					ELAY3s_2.BO			
r i i i i i i i i i i i i i i i i i i i			,,		R1.BO			
•	< <stat< td=""><td>ement role&gt;&gt;</td><td>Cancel</td><td>Delete</td><td>K2.BO</td></stat<>	ement role>>	Cancel	Delete	K2.BO			
L								
					SWSDIOOOT_MANDAL_COMMAND			
					SWSPI0002 LOW MAIN STEM PRESSURE			
					SWSPI0003 HIGH REACTOR OUTPUT TEL			
					SWSPI0004 REACTOR P TOO HIGH			
					SWSPI0005 RATE OF CHANGE IN P TOC			
					U_DELAY1.BO			
Example of Universality after: Condi	ition <	roposition> holds after condition <proposition></proposition>		•				
Example of Universality_after: Condition <proposition> holds after condition <proposition>.</proposition></proposition>								



## Statements and corresponding temporal logic formulas

Statement S-001 Sentence Condition START\_ACT holds after condition SWSOI0001\_MANUAL\_COMMAND until condition ( DELAY15s.clock is equal\_to 0 ). Statement S-002 Sentence Condition ( OR2.BO implies ( DELAY3s\_2.BO OR DELAY15s.BO ) ) holds globally. Statement S-003 Sentence Condition START\_ACT remains false before condition ( OR2.BO OR SWSOI0001\_MANUAL\_COMMAND ). Statement S-004 Sentence Condition ( START\_ACT implies DELAY3s.BO ) holds after condition NOT DELAY3s\_2.BO until condition ( DELAY15s.clock is equal to 0 ).

```
LTLSPEC G(SWSOI0001_MANUAL_COMMAND & !(DELAY15s.clock = 0) -> (G(START_ACT) | (START_ACT U
(DELAY15s.clock = 0))))
LTLSPEC G((OR2.BO -> (DELAY3s_2.BO | DELAY15s.BO)))
LTLSPEC F(OR2.BO | SWSOI0001_MANUAL_COMMAND) -> (!START_ACT U (OR2.BO | SWSOI0001_MANUAL_COMMAND))
LTLSPEC G(!DELAY3s_2.BO & !(DELAY15s.clock = 0) -> (G((START_ACT -> DELAY3s.BO)) | ((START_ACT ->
DELAY3s.BO) U (DELAY15s.clock = 0)))
```



#### **Conclusions / further research**

- What kind of templates would be needed for real-world I&C systems?
- How should the CNL editor be implemented and integrated with other design / analysis tools?
- How should the user interface look like?
- Practical evaluation is needed!

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- STUK Radiation and Nuclear Safety Authority, Finland
- Evaluation of NPP I&C system designs 2008-2011
- Fortum, Power company, Finland
- Verification of nuclear I&C by model checking 2012-



**STUK**