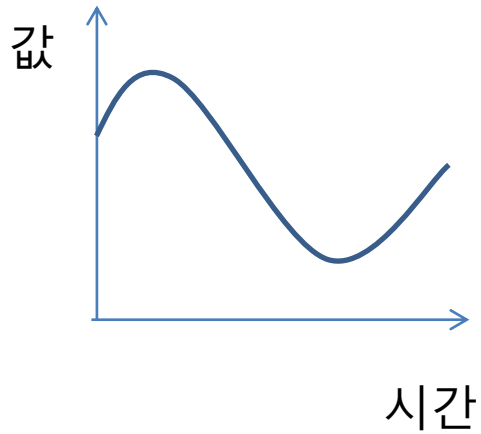


Case Study – HyTech를 이용하여 자동 차간 거리 조절

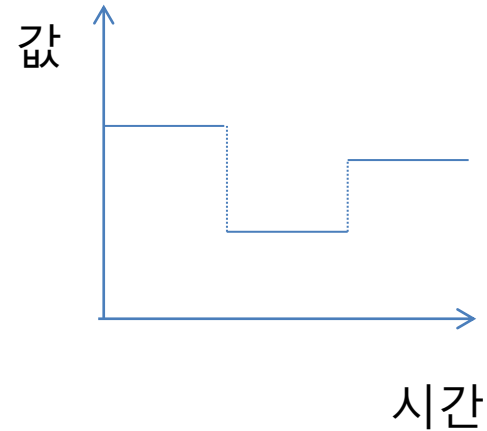
Jaeyeon Jo

Background

- Hybrid System
 - Continuous element – Physical, Electric, Analog
 - Discrete Element – Digital electronic, Computing, Software



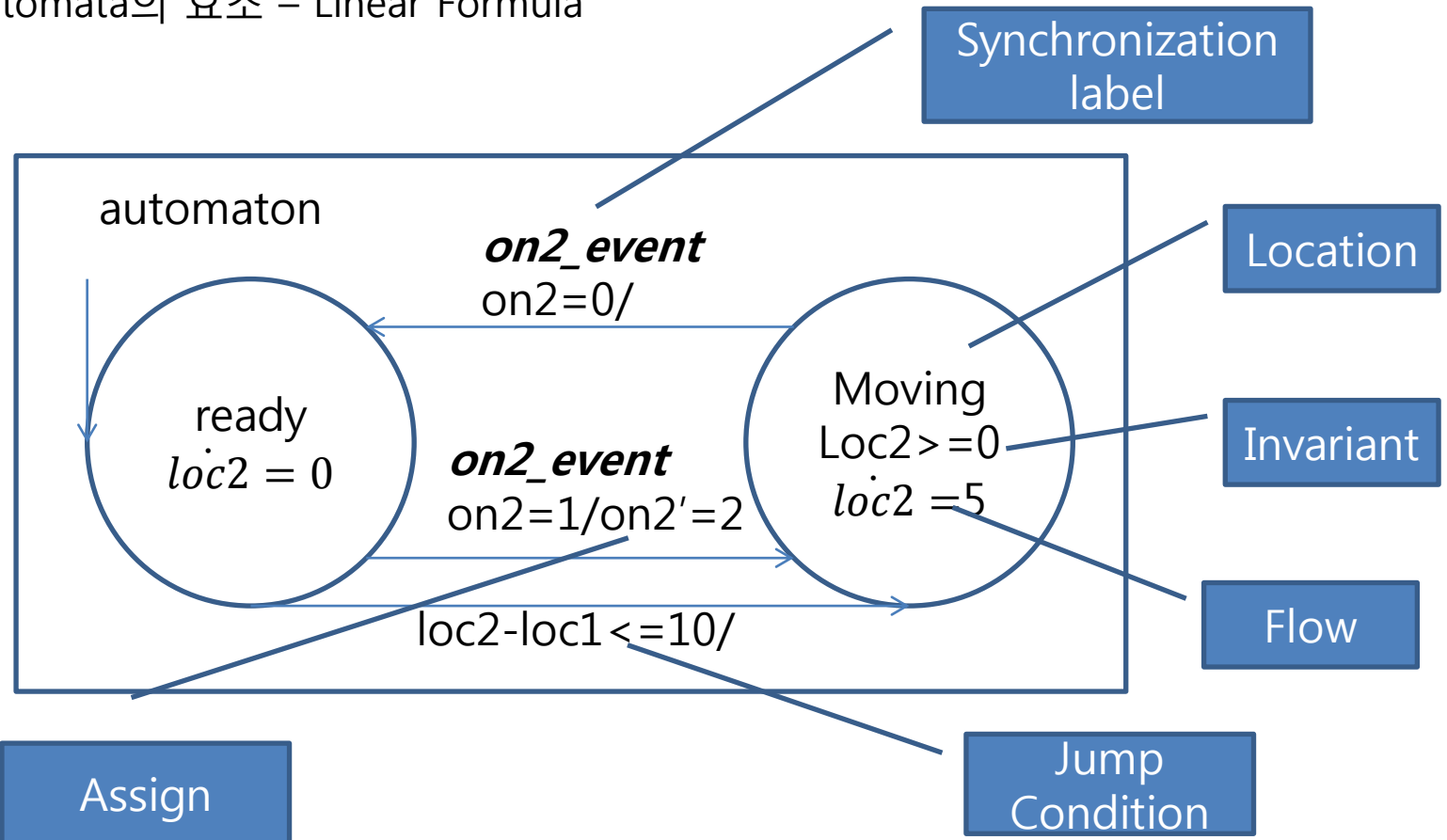
Analog Variable



Discrete Variable

Linear Hybrid Automata

- Linear Hybrid Automata
 - Hybrid System을 Automata의 형식으로 표현
 - Automata의 요소 - Linear Formula

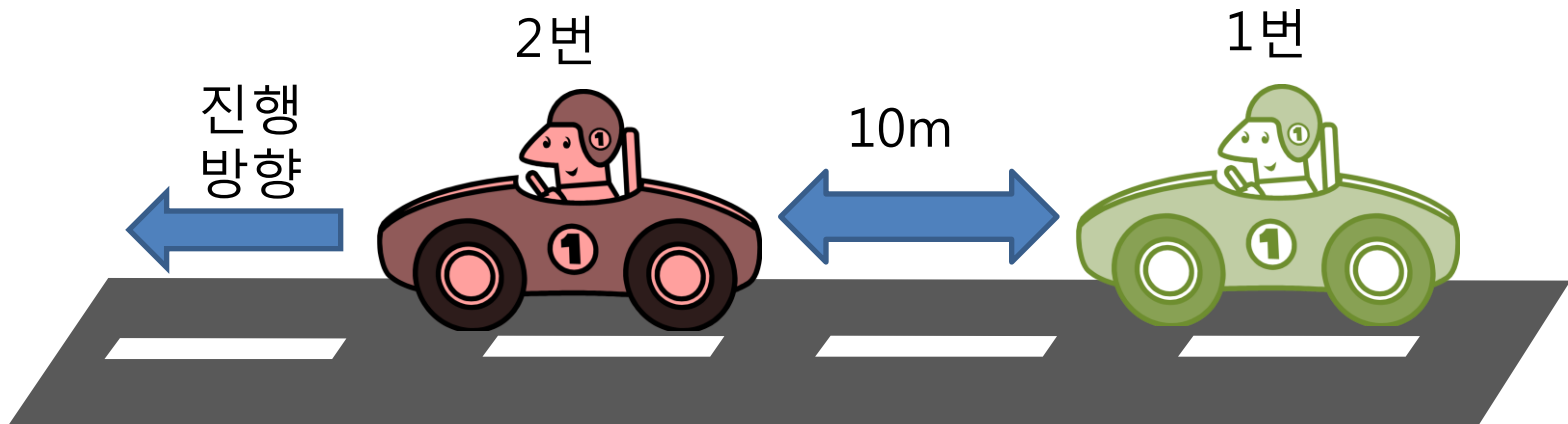


HyTech

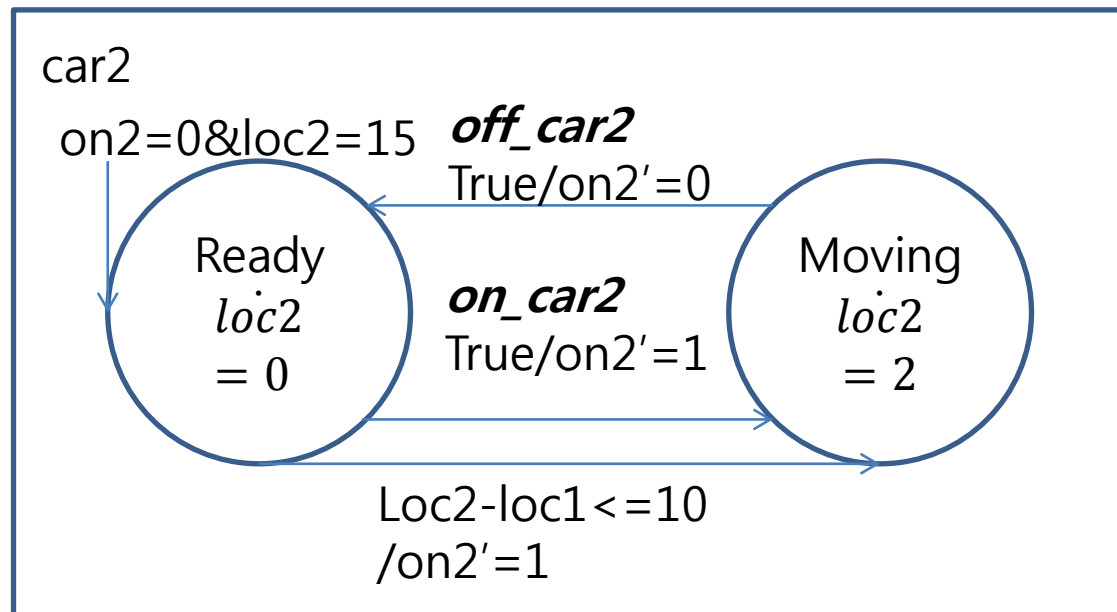
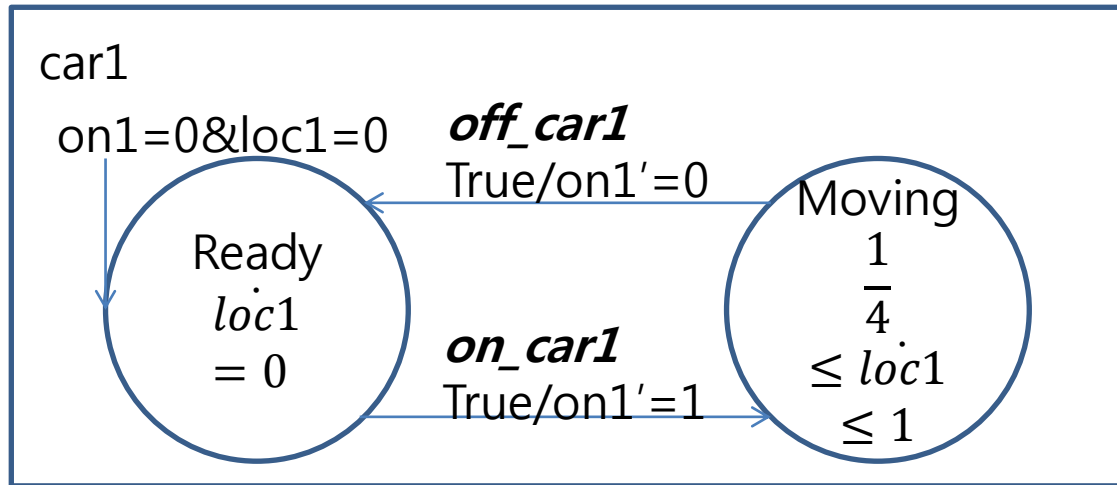
- Hybrid System을 검증, 분석하는 도구
- 입력 언어 – Linear Hybrid Automata
 - Variables, Locations, Initial Condition, Invariant Conditions, Transitions, Rate Conditions
- Parallel composition
 - Synchronizing Hybrid Automata
- Reachability and safety verification
 - Forward/Backward reachability
- Parametric analysis

Model : Car control model

- Example – Car Controller 예제
 - 안전성 위반 조건 : car1과 car2의 거리가 10 이하
 - 안전성 위반 조건 검사 : 초기 상태에서부터 해당 상태까지 갈수 있는가?
 - 해당 상태까지 갈 수 있음 : 안전성 위반
 - 해당 상태까지 갈 수 없음 : 안전성 유지
 - 초기 상태를 설정하지 않는다면 해당 상태까지 제약 없이 갈 수 있음
 - 초기상태를 설정할 필요가 있음



Automata



HyTech Code

var

```
loc1:analog;
loc2:analog;
```

HyTech에서 검증
할 Automata가 사
용할 Variable 선언

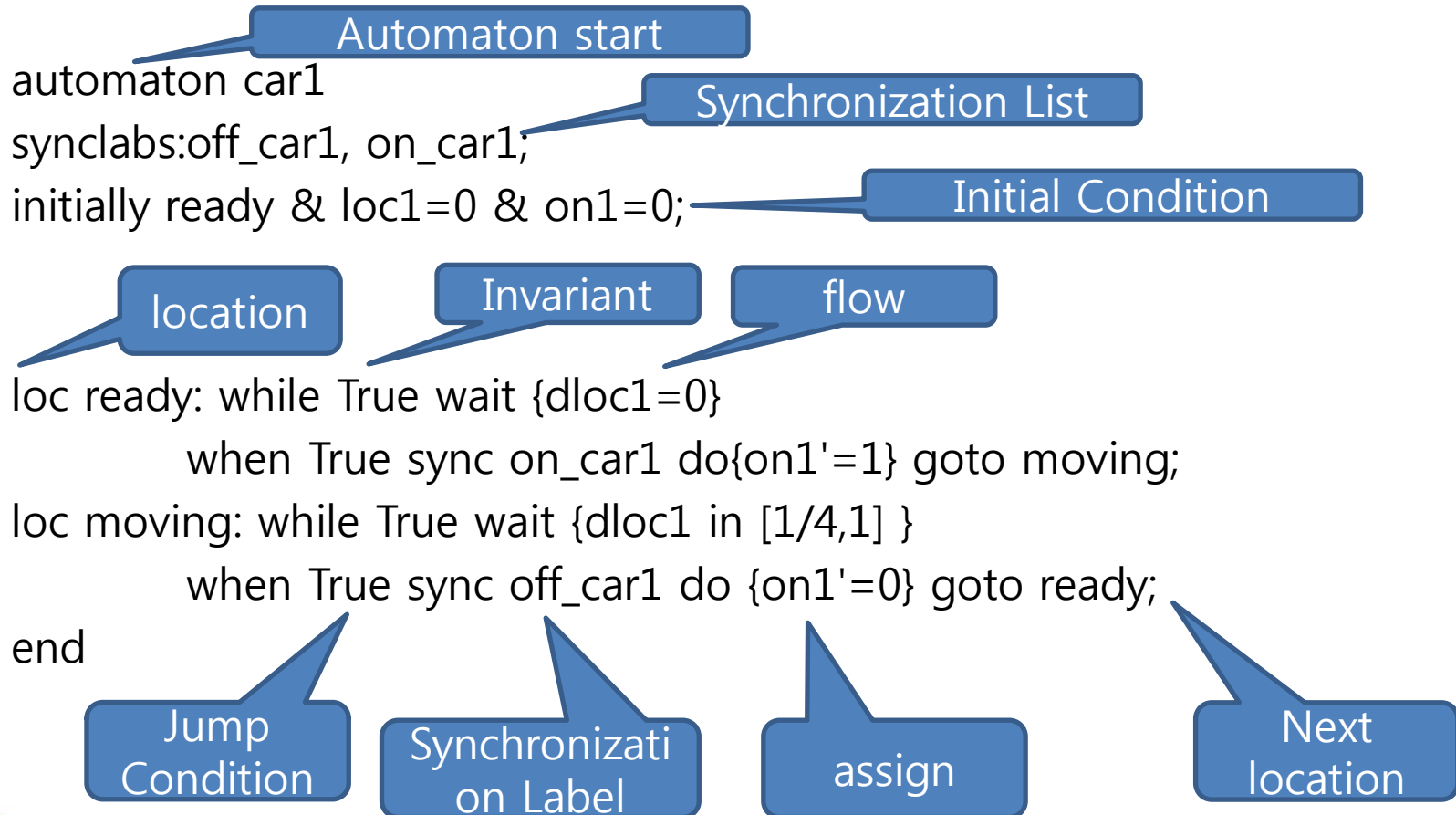
```
on1:discrete;
on2:discrete;
```

Rate에 따른 variable 분류

analog : flow에서 정할 수 있음

discrete : rate가 0이고 transition에서만 값 변경 가능

HyTech Code



Properties

var init_reg, final_reg, reached_reg : region;

Region definition

init_reg := loc[car1]=ready & loc[car2]=ready & loc1=0 & loc2=15 & on1=0
& on2=0 & t=0;

Initial region

final_reg := loc2-loc1 < 10;

Verification
property

reached_reg := reach forward from init_reg endreach;

All reachable
region from
init_reg

print trace to final_reg using reached_reg;

Verification
Code

Verification

- Reached State List
 - Initial condition으로부터 도달할 수 있는 모든 상태를 나타냄
 - Moving, Moving상태에서 가능한 모든 변수의 값들을 나타냄

```

~/hytech-win
=====reached_reg=====
====
Location: moving.moving
  on2 = 1 & on1 = 1 & loc2 <= 2t + 15 & loc2 >= 2loc1 + 15 & loc1 >=
0
|
|
  on1 = 1 & on2 = 1 & loc2 <= 2t + 5 & 8loc1 >= loc2 + 25 & loc2 >= 2
loc1 + 5
|
|
  on1 = 1 & on2 = 1 & loc2 <= 2t + 15 & loc2 <= 8loc1 + 15 & loc1 <=
t & loc2 >= 15 & loc2 >= 2loc1 + 5
|
|
  on2 = 1 & on1 = 1 & loc2 <= 2t + 5 & loc2 >= 2loc1 + 5 & loc1 >= 5
|
|
  on1 = 1 & on2 = 1 & loc2 >= 2loc1 + 5 & loc2 >= 15 & loc1 <= t &
loc2 <= 2t + 15 & loc1 >= 0
|
|
  on1 = 1 & on2 = 1 & loc2 >= loc1 + 10 & loc2 + 2t >= 4loc1 + 5 & lo
c2 <= 2t + 5 & 8loc1 >= loc2 + 25
|
|
  on1 = 1 & on2 = 1 & loc2 + 2t >= 4loc1 + 5 & loc2 >= loc1 + 10 & lo
c2 >= 15 & loc1 <= t & loc1 >= 0 & loc2 <= 2t + 15
|
|
  on1 = 1 & on2 = 1 & loc2 >= loc1 + 10 & loc1 <= t & loc2 <= 2t + 5
& 8loc1 >= loc2 + 25
|
|
  on1 = 1 & on2 = 1 & loc1 <= t & loc1 >= 0 & loc2 <= 2t + 15 & loc
2 >= 15 & loc2 >= loc1 + 10
Location: moving.ready
  on2 = 0 & loc2 = 15 & on1 = 1 & loc1 <= t & loc1 <= 5 & loc1 >= 0
|
|
  on2 = 0 & on1 = 1 & 2loc1 + loc2 <= 2t + 15 & loc2 >= 15 & loc1 >=
$
    
```

Verification

- Final_reg
 - 검증할 속성을 나타냄
 - 이 상태에 도달할 수 있다면, 검증속성을 만족하지 못함

- No path to....
 - 초기 상태에서부터 Final_reg까지 도달할 수 없을 나타냄
 - 만약 Final_reg까지 도달 가능하다면 도달하는 시나리오 중 하나를 보여줌 (Counter Example)

```

~/hytech-win
on2 = 0 & on1 = 0 & 2loc1 + loc2 <= 2t + 15 & loc2 >= 15 & loc2 >=
loc1 + 10 & loc1 >= 0
!
on2 = 0 & on1 = 0 & loc1 <= t & loc2 >= 15 & loc2 >= loc1 + 10 &
loc1 >= 0 & loc2 <= 2t + 15
=====final_reg=====
==
Location: moving.moving
loc2 < loc1 + 10
Location: moving.ready
loc2 < loc1 + 10
Location: ready.moving
loc2 < loc1 + 10
Location: ready.ready
loc2 < loc1 + 10
===== Generating trace to specified target region =====
No path to indicated target region

=====
Max memory used = 4040 pages = 264765440 bytes = 252.50 MB
Time spent = 0.01u + 0.08s = 0.09 sec total
=====
Jaeyeon@Jaeyeon-PC ~/hytech-win
$

```